



PATENT

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Linsky, et al.

Confirmation No.: 9061

Application No.: 09/929,464

Examiner: Jia W. Lu

Filing Date: 14 August 2001

Group Art Unit: 2634

Title: Efficient Implementation of a Decision Directed Phase Locked Loop (DD-PLL) for Use with Short Block Code in Digital Communication Systems

Mail Stop Issue Fee  
Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

DRAWING TRANSMITTAL

Sir:

Enclosed herewith please find:

- ☐ \_\_\_\_\_ sheets of redlined drawing(s) which indicate proposed changes to the drawing(s). Upon approval of these proposed changes, formal drawing(s) will be submitted.
- ☐ \_\_\_\_\_ sheets of corrected formal drawing(s), as required by the Notice of Patent Drawings Objection (PTO-948) which accompanied the Office Action dated \_\_\_\_\_.
- ☐ \_\_\_\_\_ sheets of corrected formal drawing(s), as required by the Notice of Patent Drawings Objection (PTO-948) and approved in the Notice of Allowability dated \_\_\_\_\_.
- ☐ \_\_\_\_\_ sheets of formal drawing(s). Please substitute these formal drawing(s) for the informal drawing(s) originally filed.
- ☒ 1 sheet of corrected formal drawings (Figure 8) in compliance with Examiner's Amendment included in the Notice of Allowance dated June 6, 2005.

Examiner's approval of the entry of these drawings is respectfully requested. No new matter has been added.

## CERTIFICATE OF MAILING (37 C.F.R. 1.8A)

I hereby certify that this correspondence is, on the date shown below, being deposited with the United States Postal Service, with sufficient postage, as first class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Date:

June 27, 2005

Lisa L. Pringle

Lisa L. Pringle  
Signature

Respectfully submitted,

By

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6/27/05

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